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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/885,878	06/20/2001	Anand G. Dabak	T131293	4511
7590	05/05/2005			
ROBERT N. ROUNTREE				EXAMINER
TEXAS INSTRUMENTS INCORPORATED				MEEK, JACOB M
P.O. Box 655474, M/S 3999				
DALLAS, TX 75265				ART UNIT
				PAPER NUMBER
				2637

DATE MAILED: 05/05/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

<b>Office Action Summary</b>	Application No.	Applicant(s)
	09/885,878	DABAK ET AL.
	Examiner	Art Unit
	Jacob Meek	2637

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

#### Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

#### Status

- 1) Responsive to communication(s) filed on 03 January 2005.
- 2a) This action is FINAL.                                    2b) This action is non-final.
- 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

#### Disposition of Claims

- 4) Claim(s) 1 - 14, 60 - 88 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) Claim(s) 1 - 14 is/are allowed.
- 6) Claim(s) 60 - 70, 75, 77 - 86, 88 is/are rejected.
- 7) Claim(s) 71 - 74, 76, 87 is/are objected to.
- 8) Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

#### Application Papers

- 9) The specification is objected to by the Examiner.
- 10) The drawing(s) filed on \_\_\_\_\_ is/are: a) accepted or b) objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

#### Priority under 35 U.S.C. § 119

- 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
  - a) All    b) Some \* c) None of:
    1. Certified copies of the priority documents have been received.
    2. Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
    3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

#### Attachment(s)

1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)	4) <input type="checkbox"/> Interview Summary (PTO-413)
2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)	Paper No(s)/Mail Date. _____
3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date _____	5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)
	6) <input type="checkbox"/> Other: _____

**DETAILED ACTION*****Response to Amendment***

1. Applicant's amendment filed January 3, 2005 has been entered.

***Specification***

2. The corrected abstract was received January 3, 2005. This abstract is accepted.

***Response to Arguments***

3. With regard to 112, 2<sup>nd</sup> paragraph rejection of claims 60 and 65, applicant's argument is persuasive, and rejection is withdrawn.

4. With regard to 112, 2<sup>nd</sup> paragraph rejection of claims 72 - 74, amended claims overcome rejection, and rejection is withdrawn.

5. Applicant's arguments with respect to claims 60 –86 have been considered but are moot in view of the new ground(s) of rejection.

6. Claim 60 – 69, 75, and 77 are rejected under 35 U.S.C. 102(e) as being anticipated by Whinnett et al (US-6,317,411).

With regard to claim 60, Whinnett discloses a circuit compromising: an input terminal coupled to receive a 1<sup>st</sup> and 2<sup>nd</sup> group of signals (see figure 3, 60 S<sub>1</sub>S<sub>2</sub> and column 2, line 61 – column 3, line 4 where this is interpreted as equivalent), a 1<sup>st</sup> output terminal coupled to receive 1<sup>st</sup> group of symbols during a 1<sup>st</sup> time (see figure 60, S<sub>1</sub>S<sub>2</sub> and column 3, lines 5 – 7), and; a 2<sup>nd</sup> output terminal coupled to receive a 3<sup>rd</sup> group of signals having a sequence during the 1<sup>st</sup> time, the 3<sup>rd</sup> group of signals compromising a transform of the 2<sup>nd</sup> group of signals, wherein the 3<sup>rd</sup> group of signals is different from the 2<sup>nd</sup> group of signals (see figure 60, -S<sub>2</sub> S<sub>1</sub> and column 3, lines 7 – 10).

With regard to claim 61, Whinnett discloses a circuit wherein each signal of each group of signals comprises a symbol (see column 2, lines 61 – 65 where this is interpreted as equivalent).

With regard to claim 62, Whinnett discloses a circuit wherein each symbol is a QPSK keyed symbol (see column 1, lines 17 – 26 and 40 – 47, where QPSK modulation is the standard for CDMA communications).

With regard to claim 63 and 64, Whinnett discloses a circuit wherein the transform of the 2<sup>nd</sup> group comprises conjugation, negation, and reversal of order in time (see column 3, lines 7 – 10).

With regard to claim 65, Whinnett discloses a circuit wherein the 1<sup>st</sup> output terminal coupled to receive the 2<sup>nd</sup> group of symbols during a 2<sup>nd</sup> time (see figure 60, S<sub>1</sub>S<sub>2</sub> and column 3, lines 5 – 7), and wherein 2<sup>nd</sup> output terminal coupled to receive a 4<sup>th</sup> group of signals having a sequence during the 2<sup>nd</sup> time, the 3<sup>rd</sup> group of signals comprising a transform of the 2<sup>nd</sup> group of signals, wherein the 4<sup>th</sup> group of signals comprising a transform of the 1<sup>st</sup> group of signals (see figure 60, -S<sub>2</sub> S<sub>1</sub> and column 3, lines 7 – 10).

With regard to claim 66 and 67, Whinnett discloses a circuit wherein the transform of the 1<sup>st</sup> group comprises conjugation, negation, and reversal of order in time (see column 3, lines 7 – 10).

With regard to claim 68, Whinnett discloses a circuit comprising symbol mapper circuit having an input terminal coupled to receive 1<sup>st</sup> sequence of data bits, the symbol mapper circuit producing the 1<sup>st</sup> and 2<sup>nd</sup> groups of signals (see figure 3, 20 and column 2, lines 61 – 66).

With regard to claim 69, Whinnett discloses a CDMA system, which utilizes QPSK modulation, whose symbols are comprised of 2 data bits.

With regard to claim 75, Whinnett discloses a circuit wherein 1<sup>st</sup> and 2<sup>nd</sup> group of signals are encoded by a Walsh code (see figure 3, 62, W<sub>1</sub>)

With regard to claim 77, Whinnett teaches a circuit wherein the output terminals are arranged for connection to antennas (see figure 3, 30, 32, 34, 36).

With regard to claims 78 – 84, the steps claimed as method are nothing more than a restatement of the function of the apparatus of claims 60 – 67, and therefore would have been obvious to one of ordinary skill in the art at the time of invention considering the aforementioned rejection of claims 60 - 67.

With regard to claim 85, Whinnett teaches a method comprising the steps of adding each 1<sup>st</sup> group of respective plurality of signals at 1<sup>st</sup> output terminal producing a 1<sup>st</sup> output signal (see figure 3, 62), and adding each 2<sup>nd</sup> transformed group of signals at 2<sup>nd</sup> output terminal thereby producing a 2<sup>nd</sup> output signal (see figure 3, 64).

With regard to claims 86 and 88, the steps claimed as method are nothing more than a restatement of the function of the apparatus of claims 75 and 77, and therefore would have been obvious to one of ordinary skill in the art at the time of invention considering the aforementioned rejection of claims 75 and 77.

### ***Claim Rejections - 35 USC § 103***

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

7. Claims 70, 76 are rejected under 35 U.S.C. 103(a) as being unpatentable over Whinnett et al (US-6,317,411).

With regard to claim 70, Whinnett teaches a circuit comprising an interleaver circuit having an input terminal coupled to receive sequence of data bits (see figure 3, 20 and column 2, lines 61 – 66). Whinnett is silent with respect to the operation of his interleaver. Interleaving techniques of data are well known in the art and the method chosen for interleaving would be a design choice for one of ordinary skill in the art.

With regard to claim 76, Whinnett teaches a circuit wherein the code applied to 2<sup>nd</sup> group of signal is orthogonal is orthogonal to code applied to 1<sup>st</sup> group of signals. With is silent with respect to the time reversal of codes. It would have been obvious to one of ordinary skill in the art at the time of invention that time reversal of codes would be a form of orthogonality.

With regard to claim 87, the steps claimed as method are nothing more than a restatement of the function of the apparatus of claim 76, and therefore would have been obvious to one of ordinary skill in the art at the time of invention considering the aforementioned rejection of claim 76.

### ***Allowable Subject Matter***

8. Claims 1 – 14 are allowed.
9. Claims 71 – 74 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

### ***Other Cited Prior Art***

10. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Ylitalo (US-6,788,661), Kuchi (US-6,542,556), Dajer (US-6,539,209), Garmonov (US-6,510,173), Popovic (US-6,804,307), and Vook (US6,834,043) all disclose method and apparatus that appear germane to applicant's invention.

***Conclusion***

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jacob Meek whose telephone number is (571)272-3013. The examiner can normally be reached on 8:00 - 4:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jay Patel can be reached on (571)272-2988. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

JMM *JMM*

*J. K. Patel*  
JAY K. PATEL  
SUPERVISORY PATENT EXAMINER